**T.Priyadarshini**

**PROFILE**

A fresh engineering graduate, seeking an opportunity as a design engineer, in an environment which enables continuous learning processes to make myself dynamic, visionary and competitive for the developing world and contribute to the growth of an organization.

**EDUCATION**

* **B.Tech Electronics and Communication Engineering**

**CGPA – 8.6 / 10 2019-2023**

Amrita Vishwa Vidyapeetham

* **Class 12** – 85% **2019**

Institution:

* **Class 10** – 98% **2017**

Institution:

**TECHNICAL INTERESTS**

Embedded Systems, VLSI Design

**PROJECTS**

**Antenna Design**

● Duration: Nov,2021 - ongoing

● Objective: To design a SIW cavity backed antenna for 2.45GHz frequency band applications

● Tools/techniques used: Ansys HFSS

**TECHNICAL SKILLS**

C, C++, Python, MATLAB, Cedar Logic, Tina, LTSpice, Ansys-HFSS

**INTERNSHIP**

**Siemens Gamesa Renewable Energy**

● Duration/Period: 1 week

● Objective: To learn how communication occurs to and from the sites of a windmill using SCADA and the sensors involved in the effective working of a windmill

● Tools/techniques used: SCADA software  
● Outcome: An overview and a better understanding of how communication takes place between varioussites was obtained.

**Tata Communications**

● Objective: To learn the implementation of real time applications of communication systems

● Outcome: A basic understanding of how communication links are established on an

international level, how switching takes place and satellite communication was obtained.

**LANGUAGES**

English, Tamil, Hindi, Malayalam

**EXTRA CURRICULAR ACTIVITIES**

Active participation and a member of Model United Nations Conferences

Active participation and a member of International Toastmasters Club

Active participation in State level Taekwondo competitions